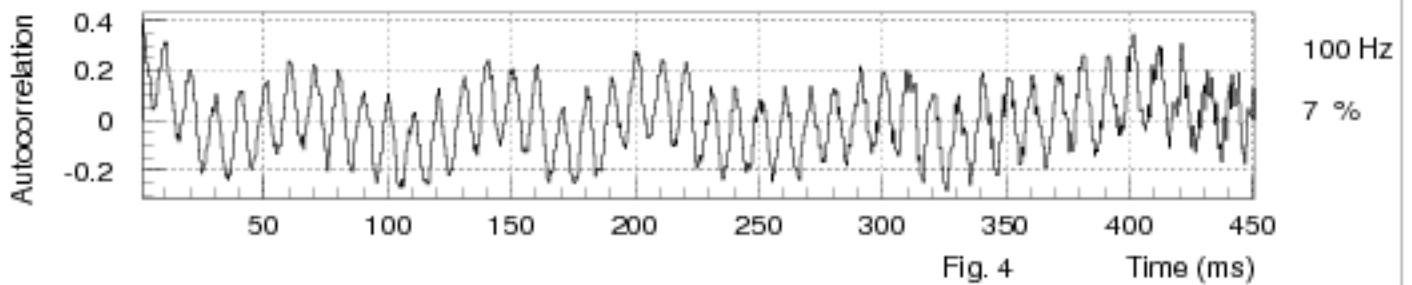
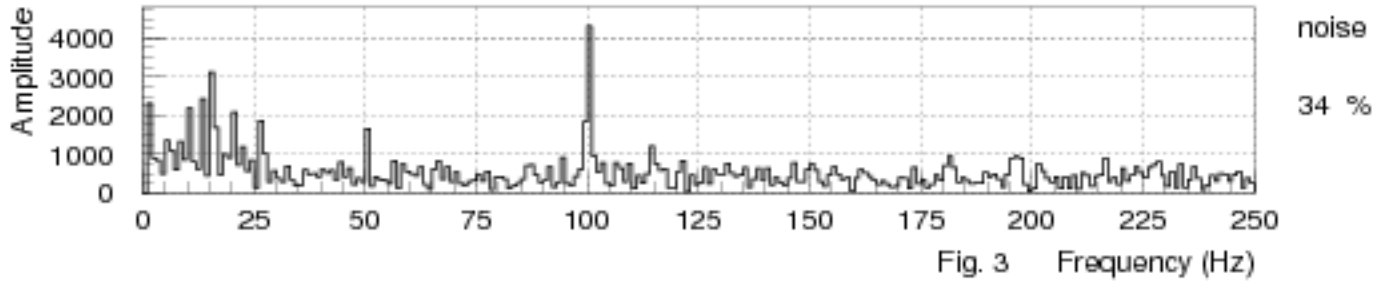
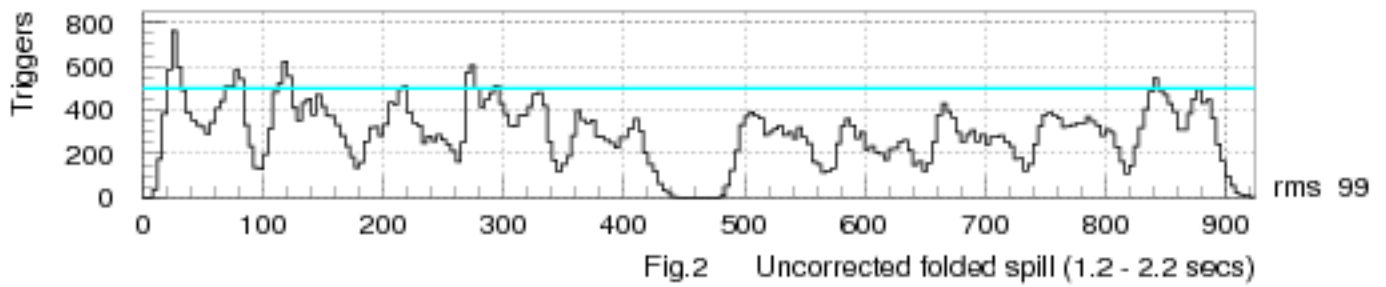
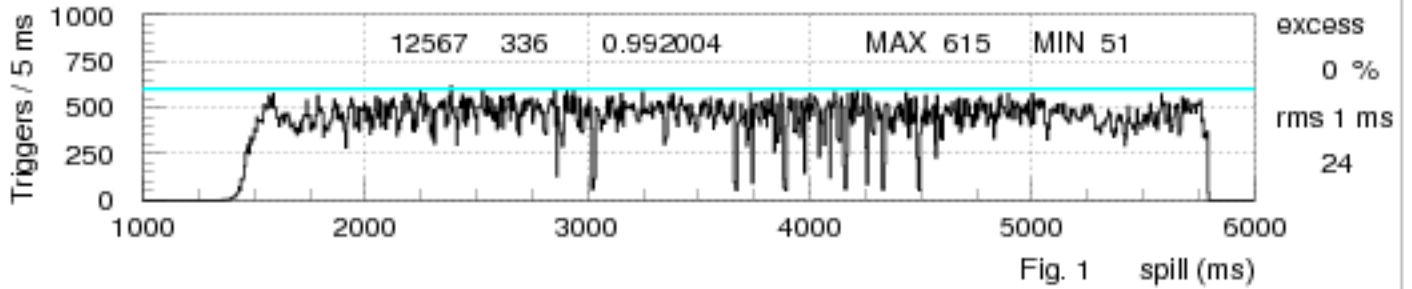
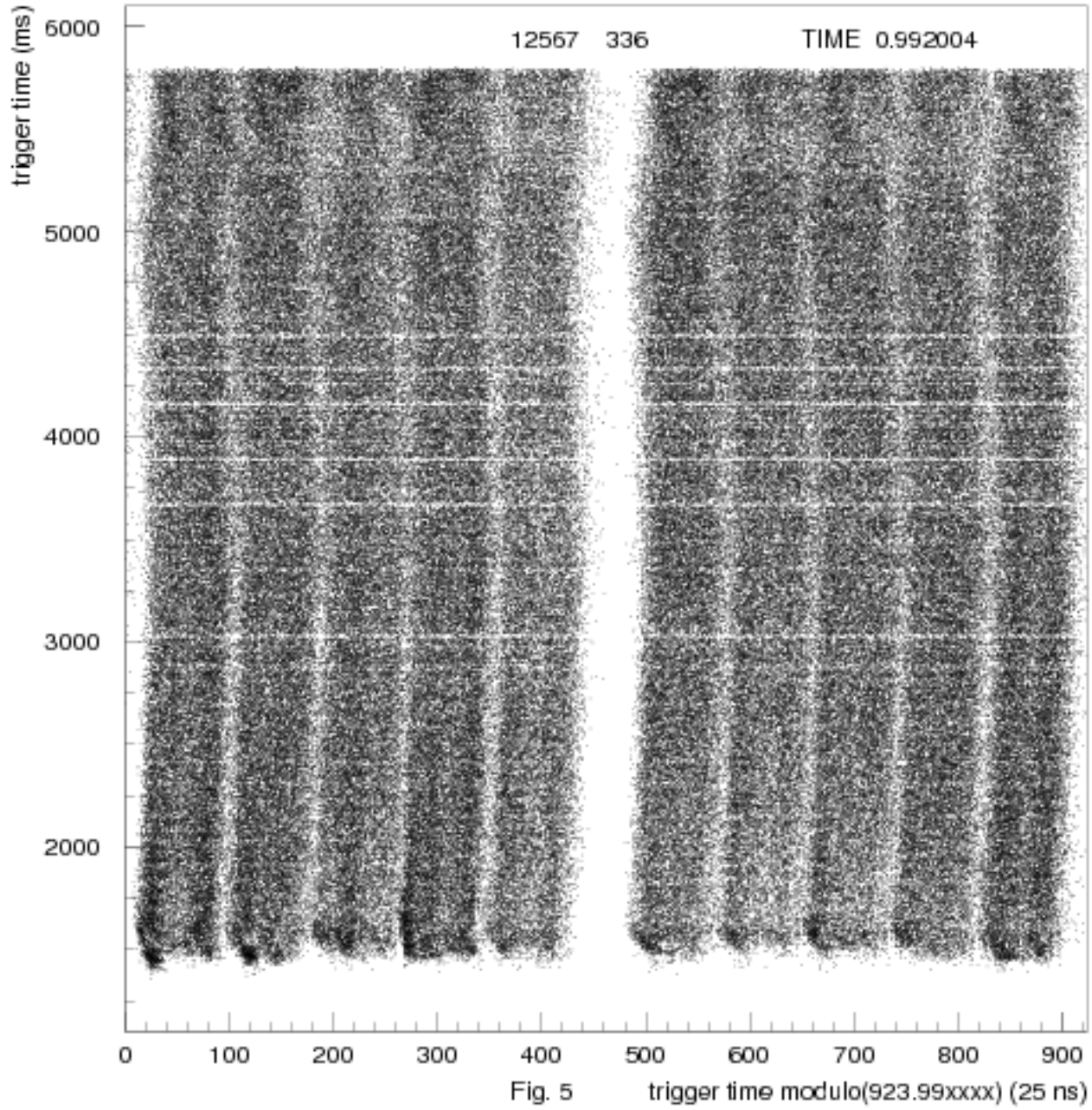


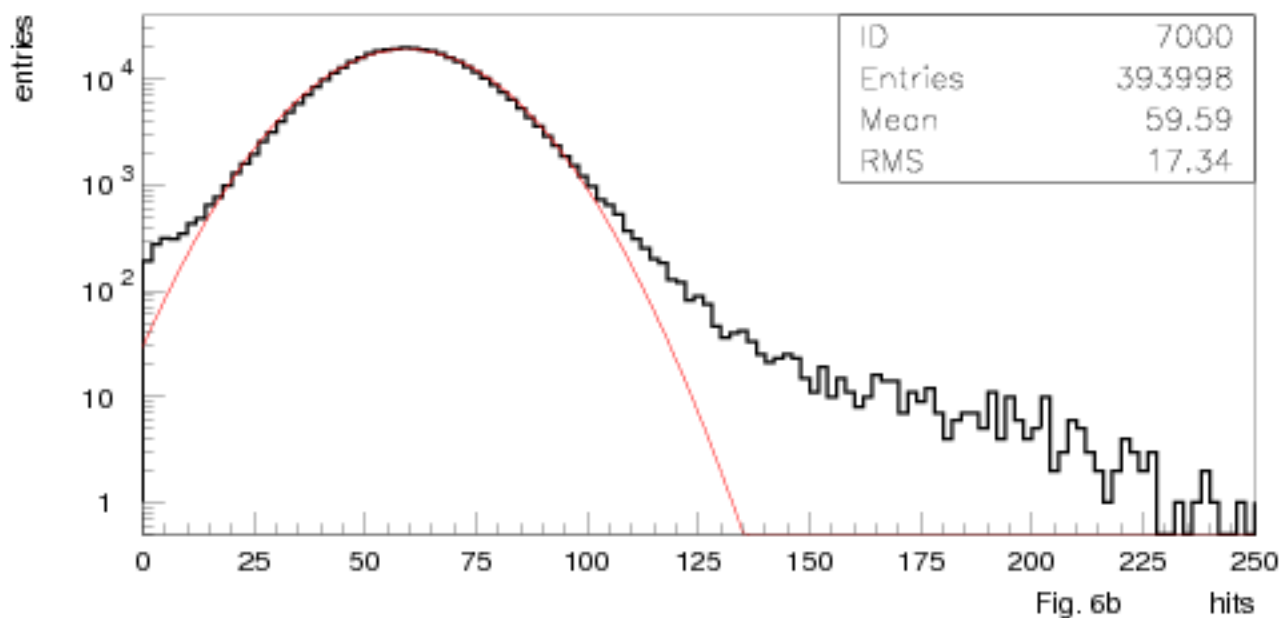
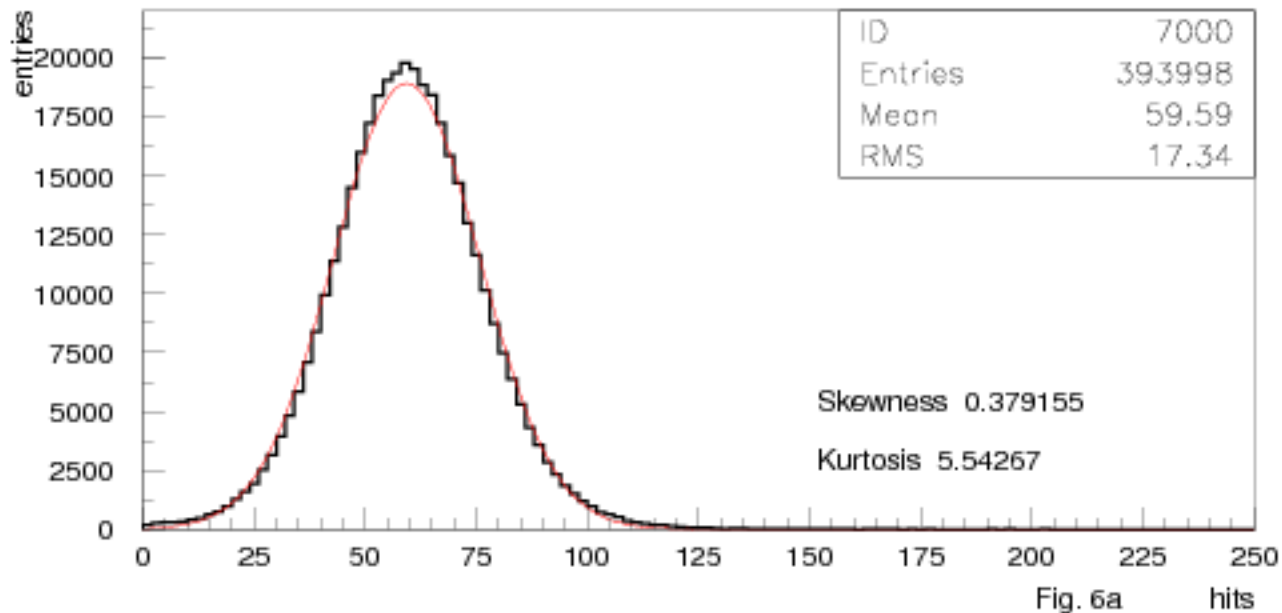
Spill - full range - 5 ms bins



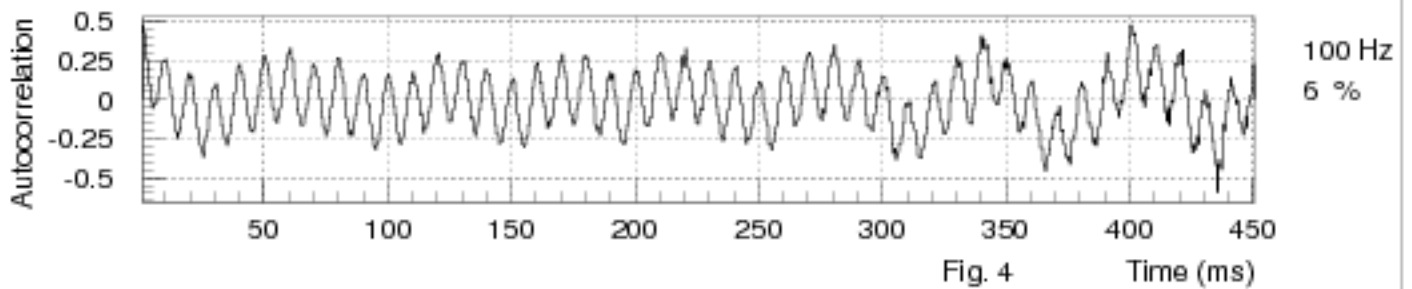
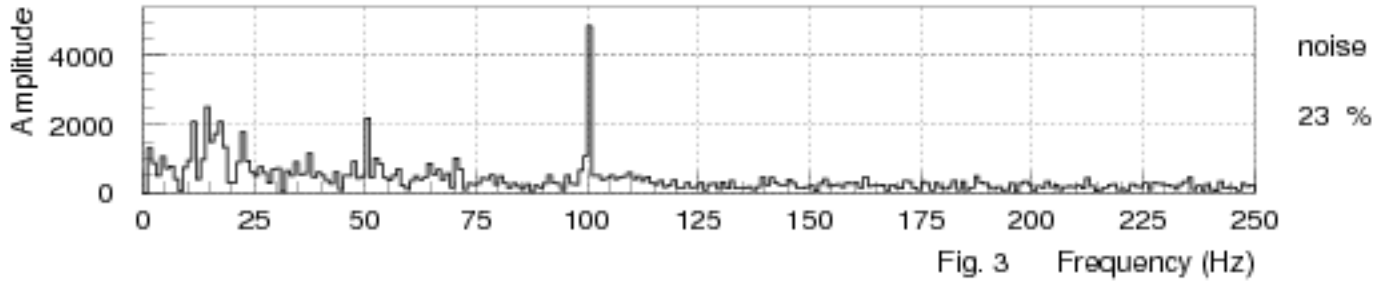
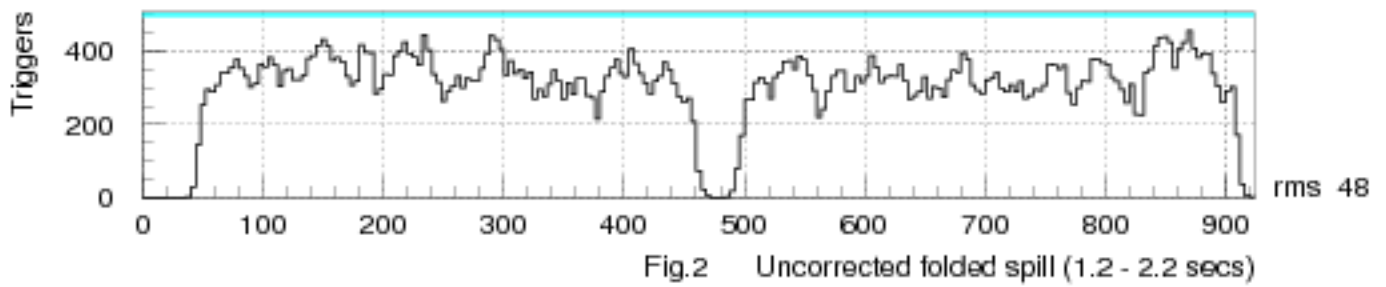
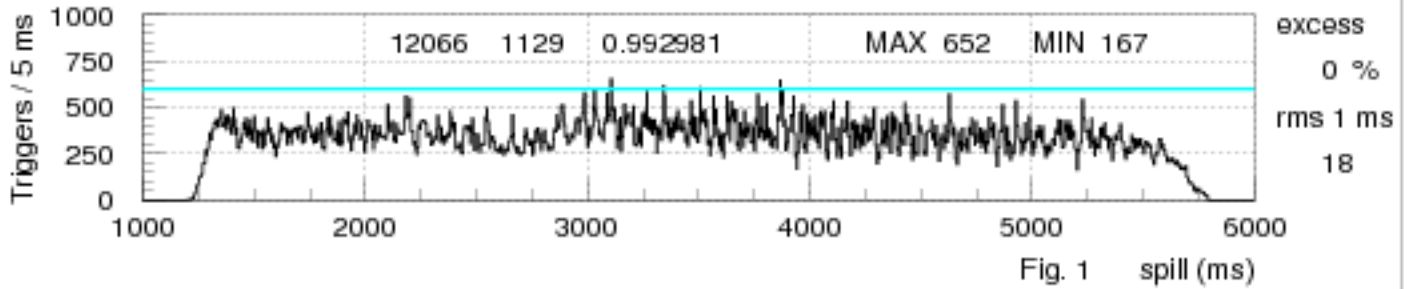
corrected spill



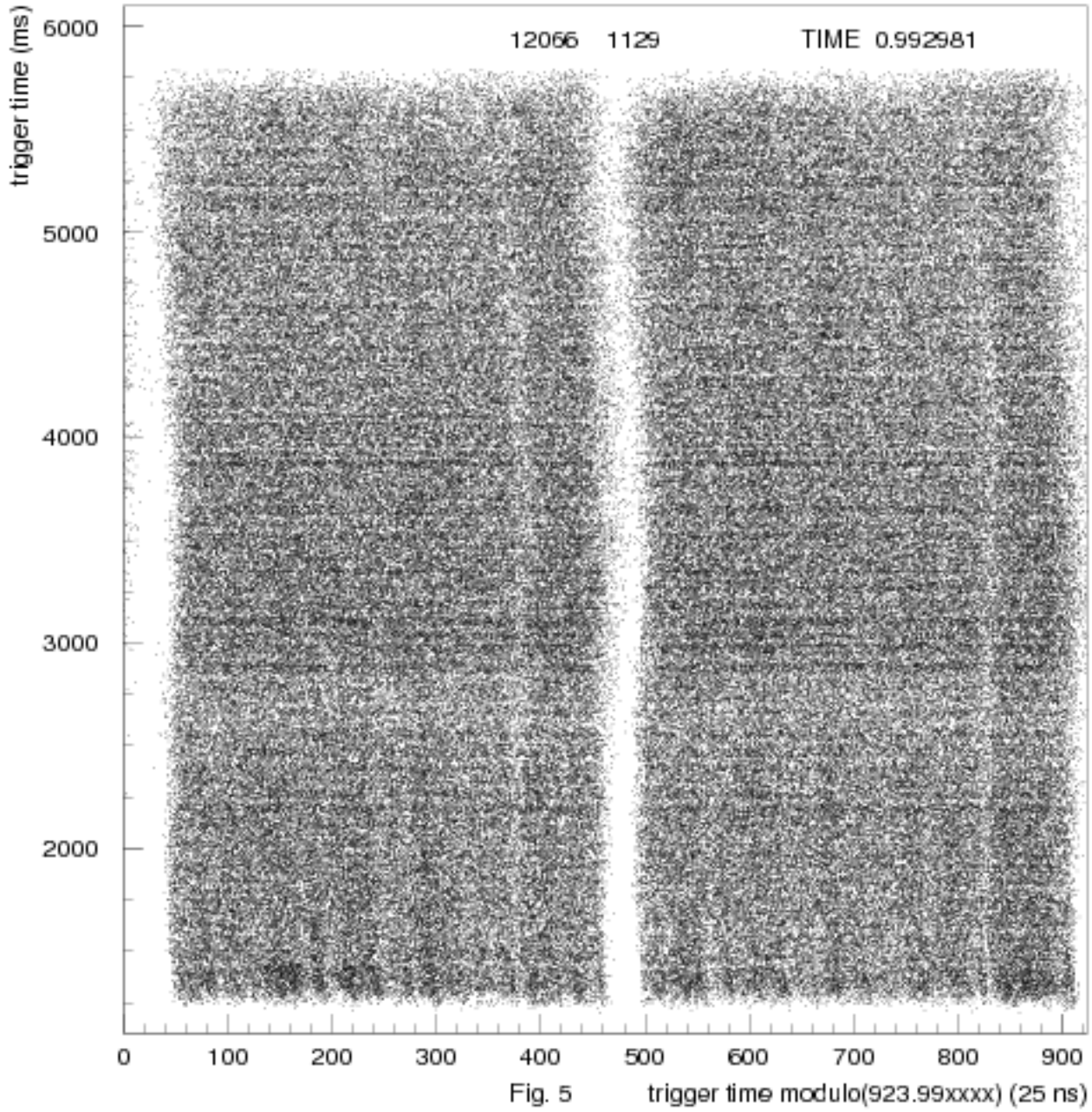
### Hits GTK(1)



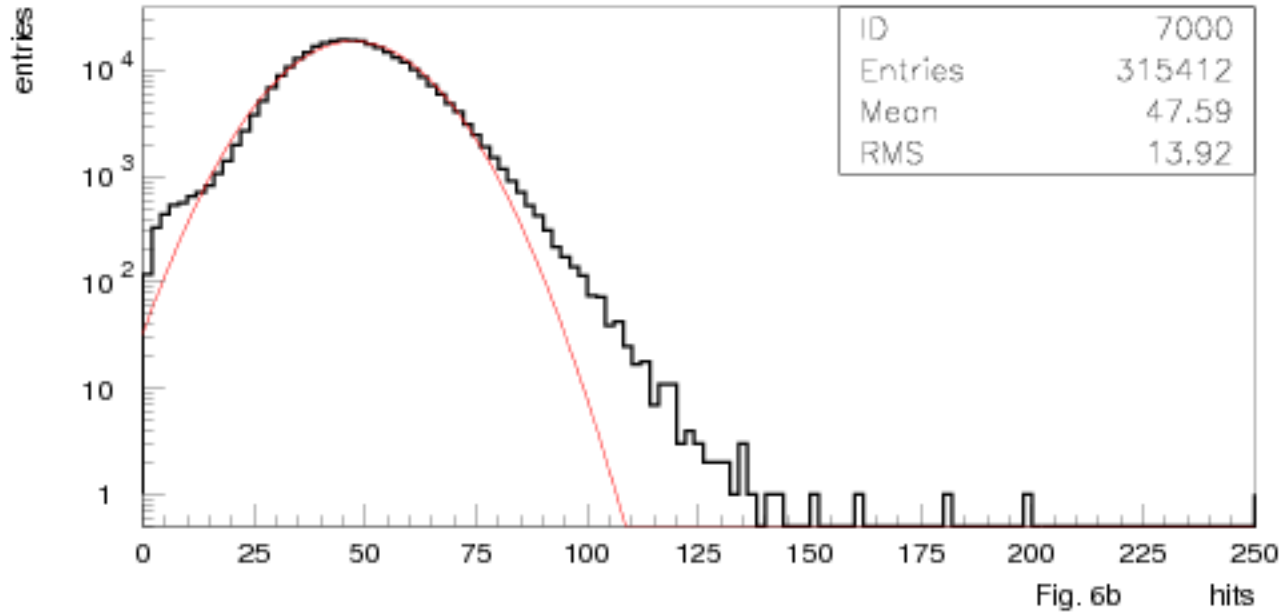
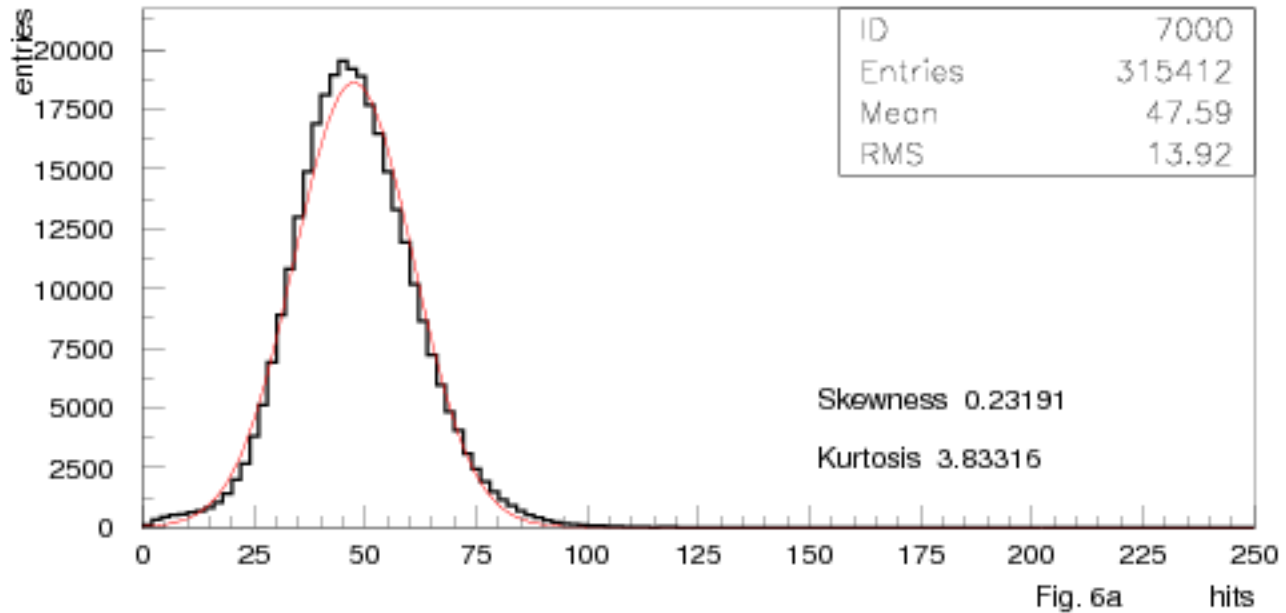
Spill - full range - 5 ms bins



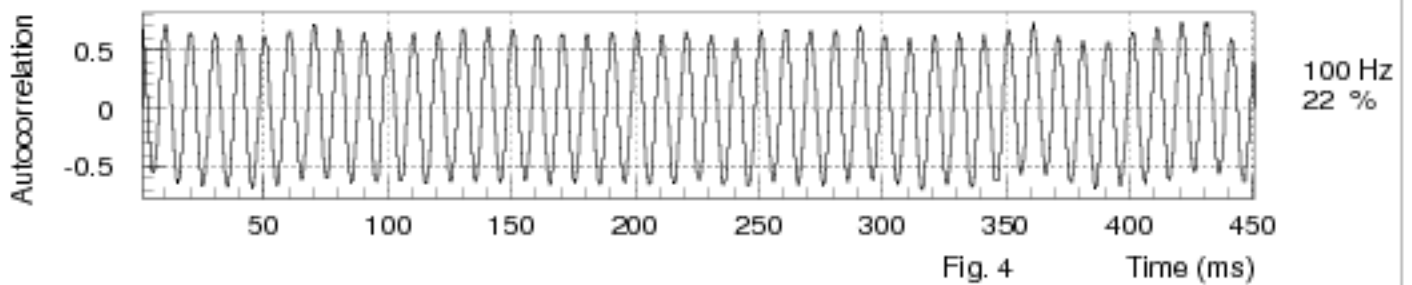
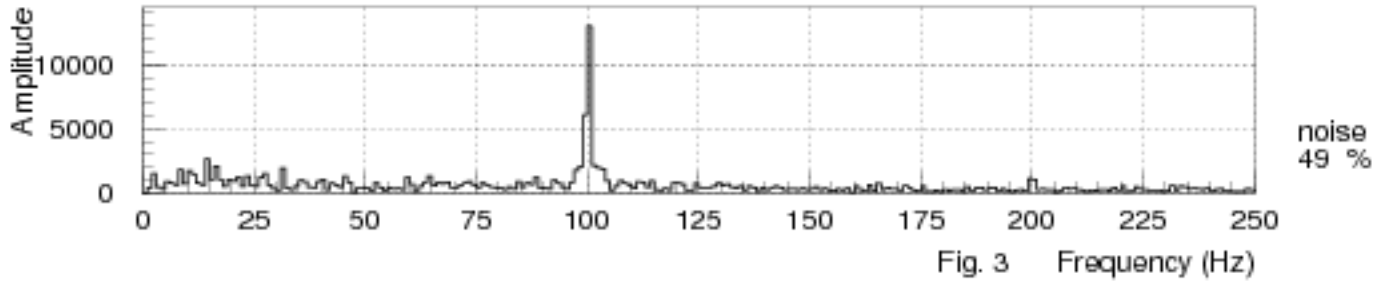
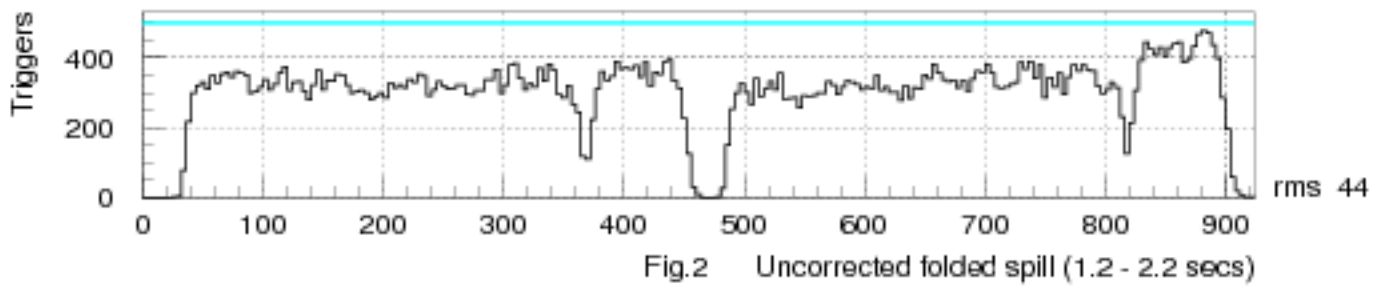
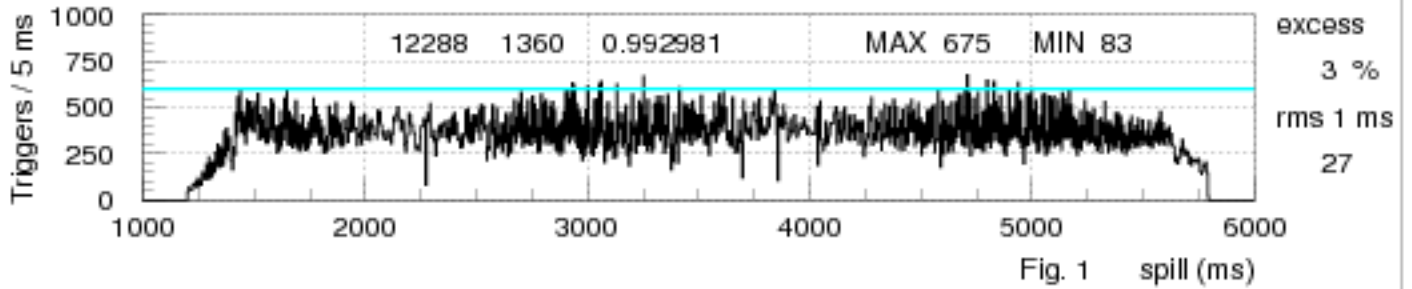
corrected spill



## Hits GTK(1)



Spill - full range - 5 ms bins





corrected spill

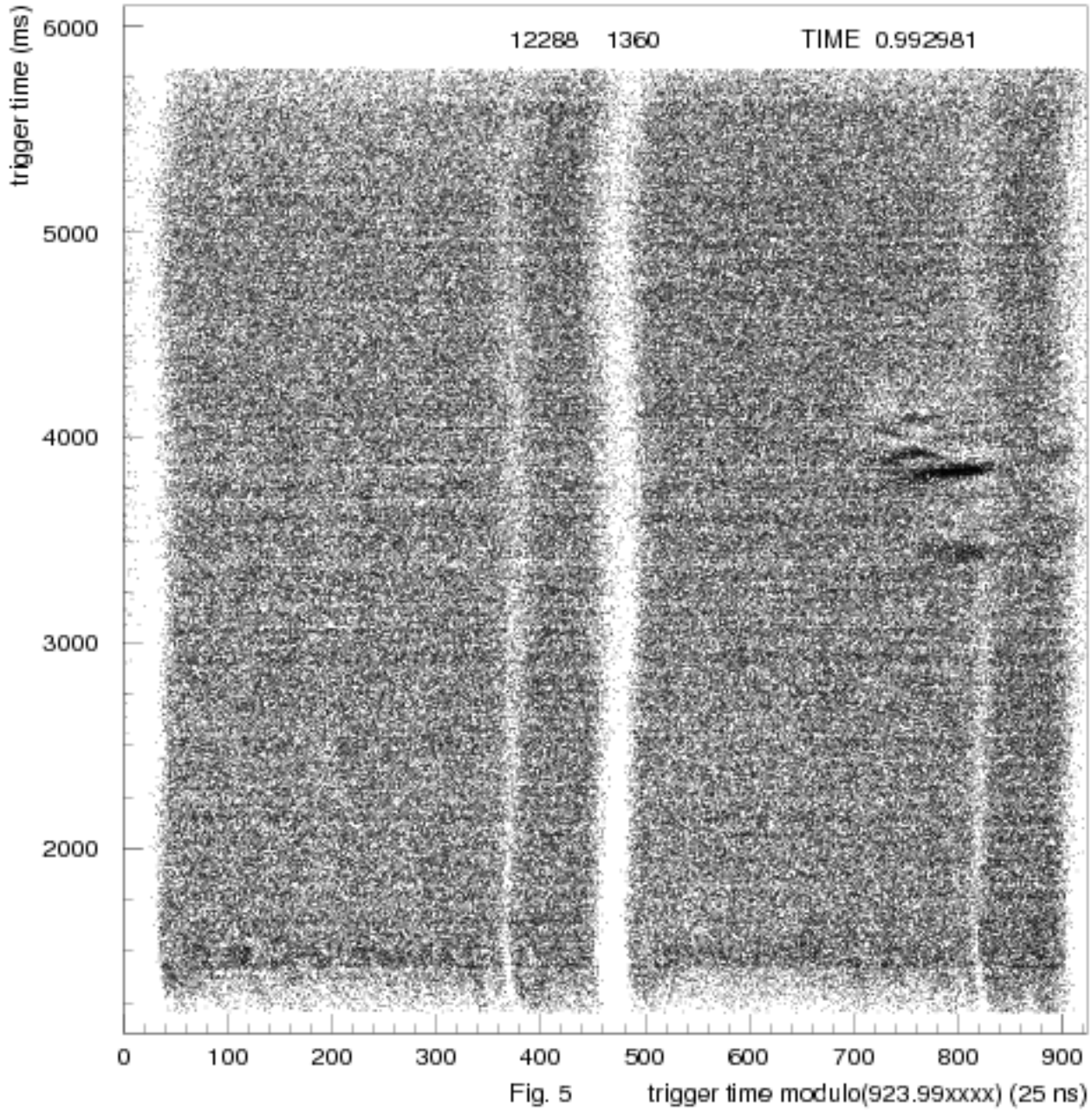
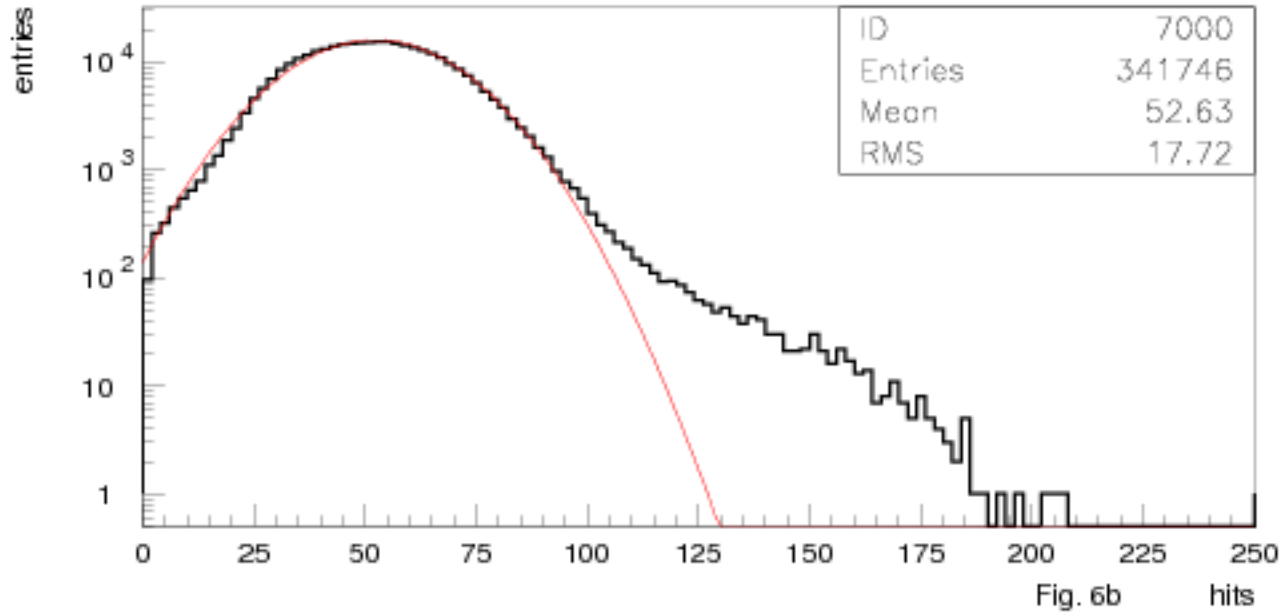
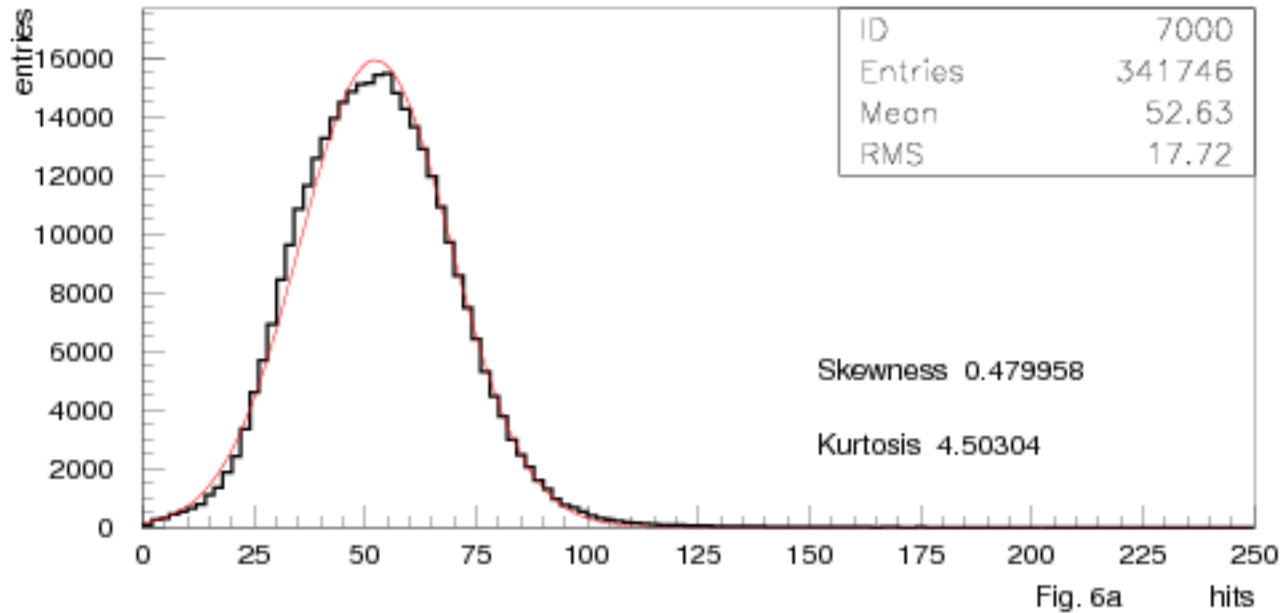


Fig. 5

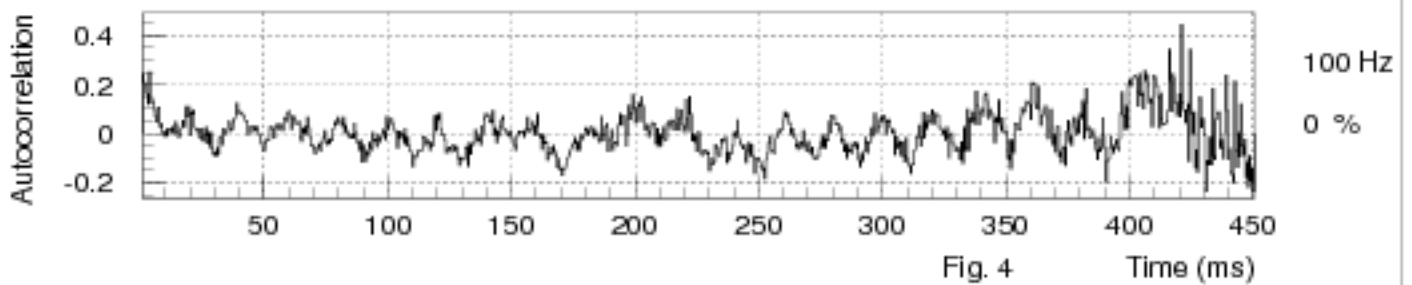
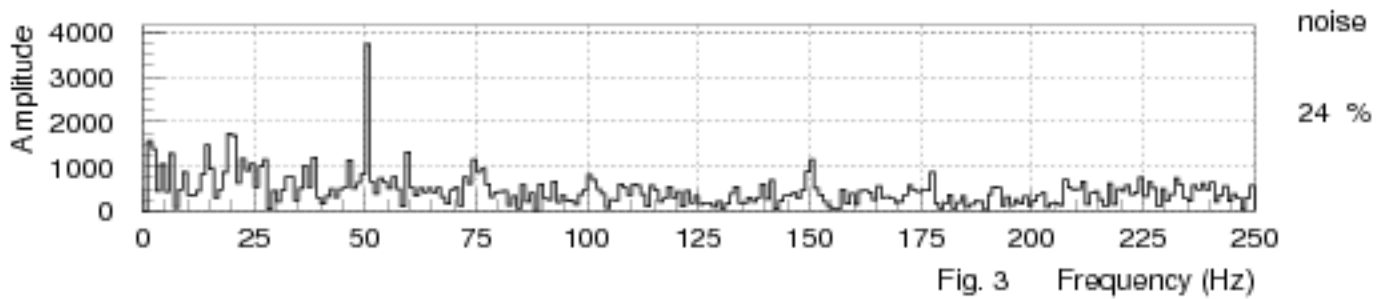
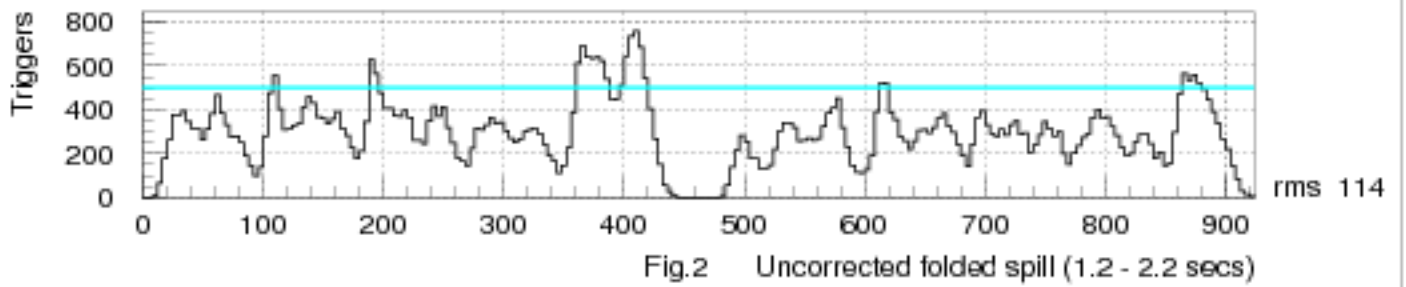
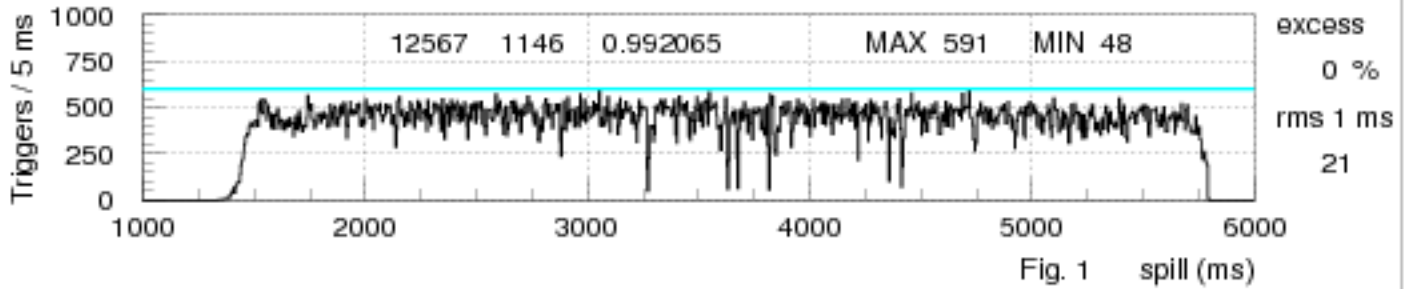
trigger time modulo(923.99xxxx) (25 ns)



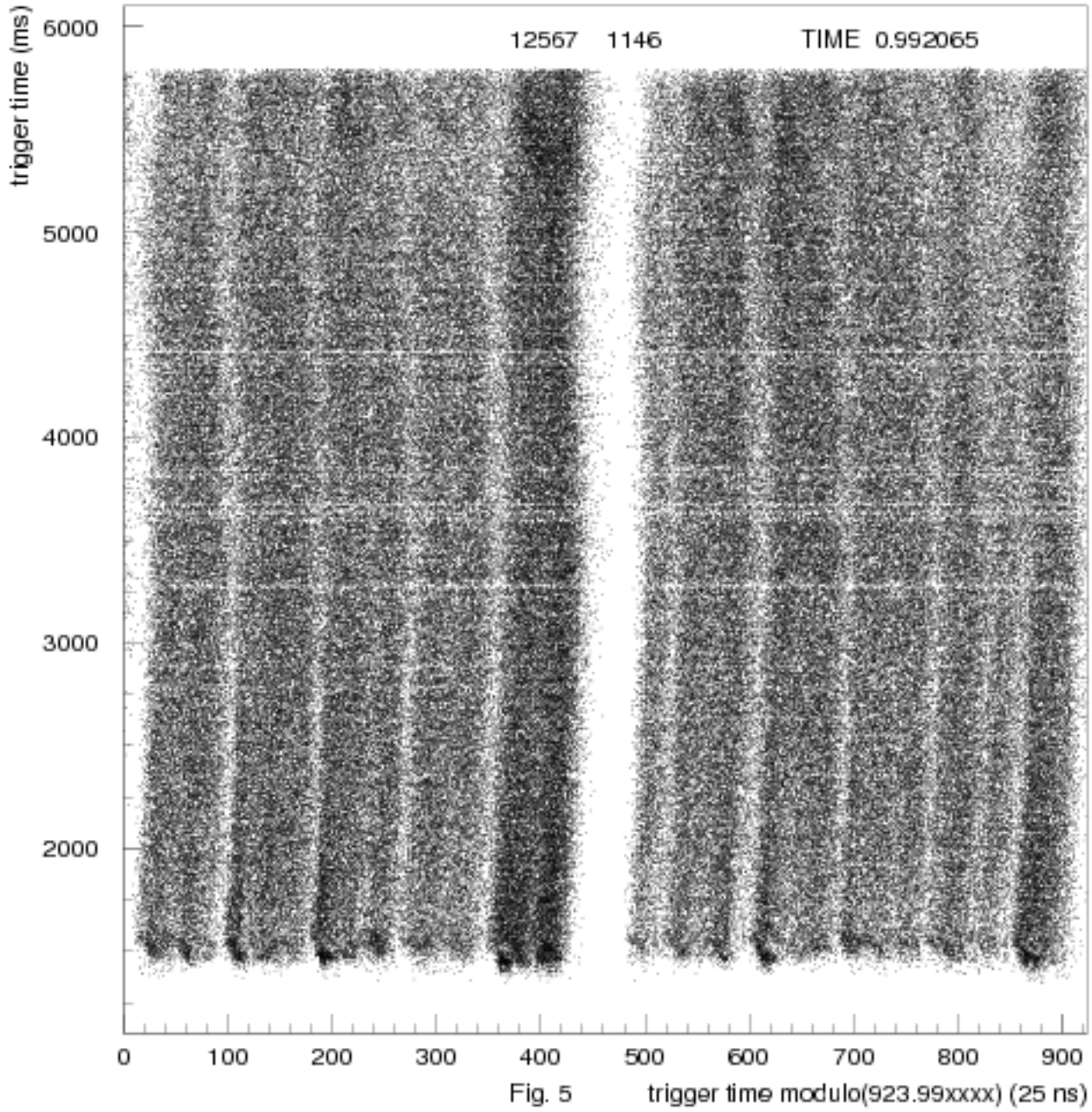
## Hits GTK(1)



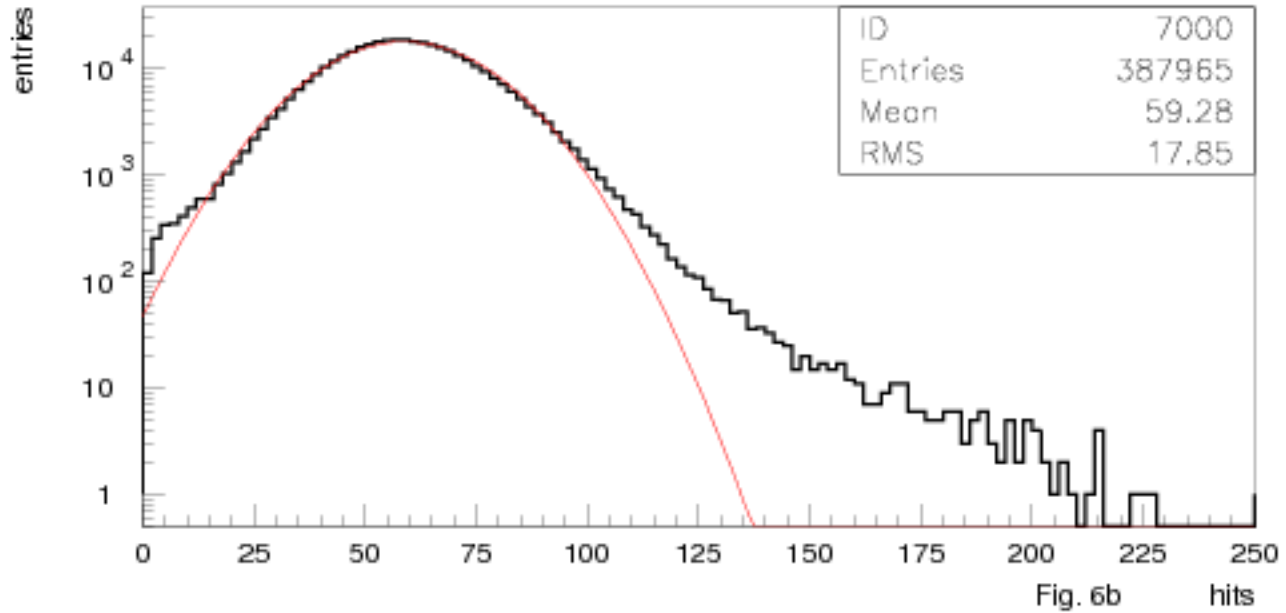
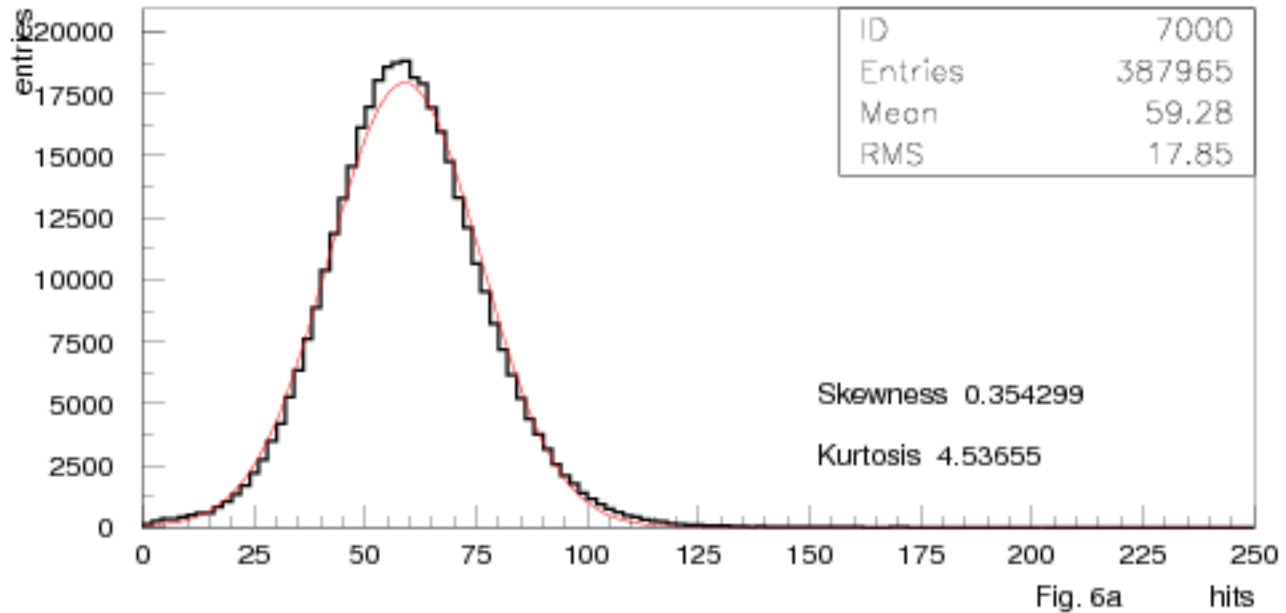
Spill - full range - 5 ms bins



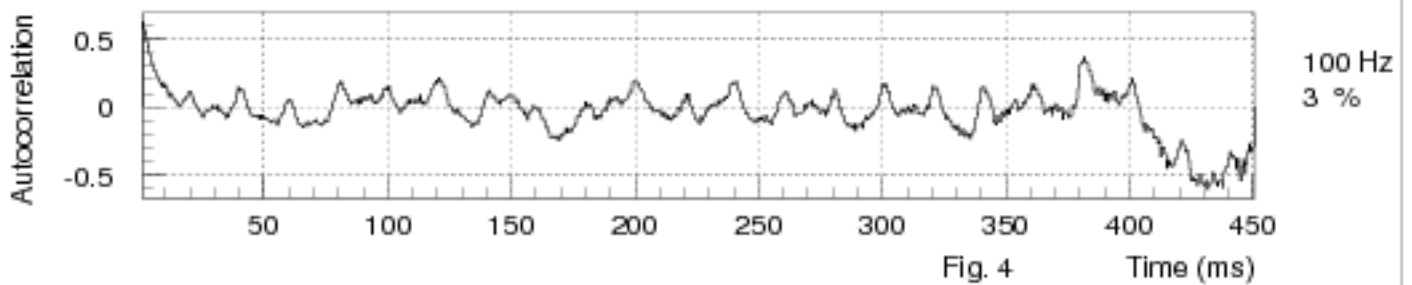
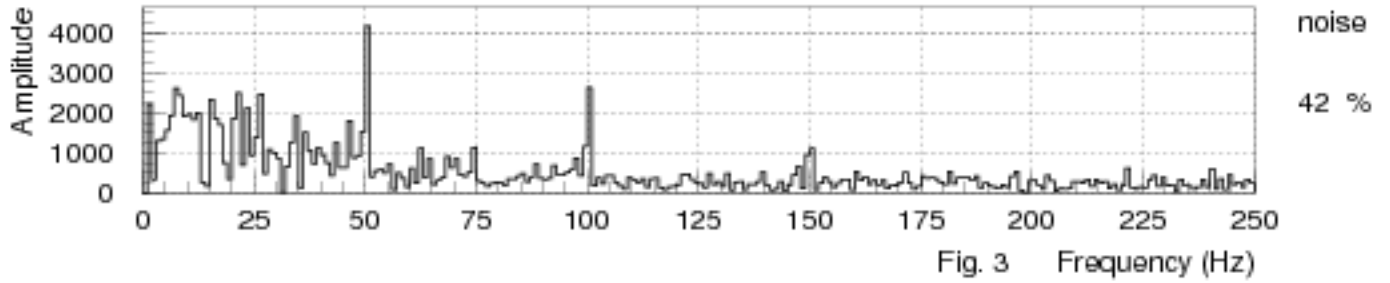
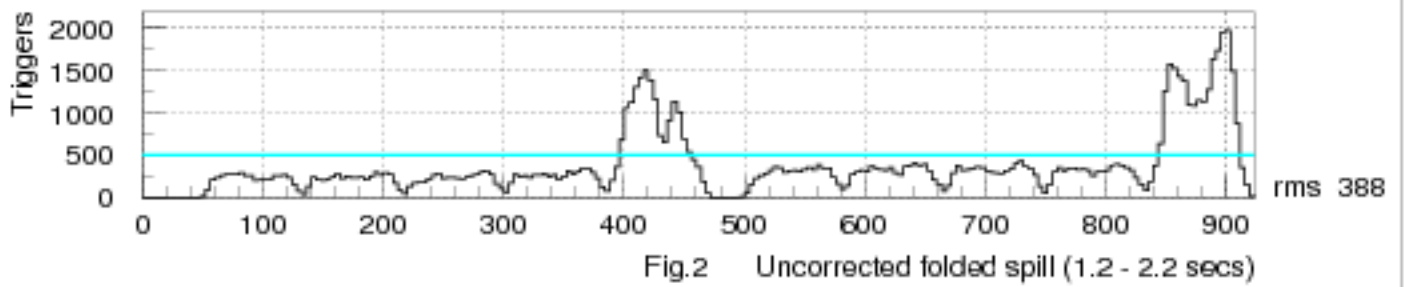
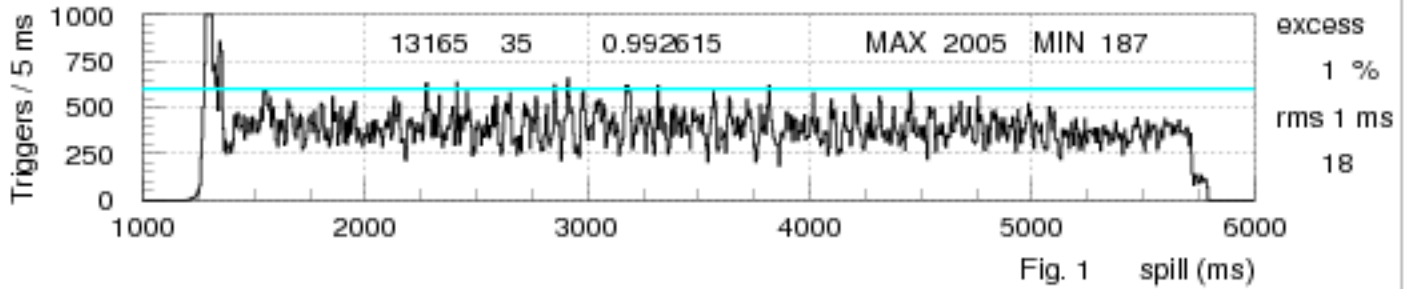
corrected spill



## Hits GTK(1)



Spill - full range - 5 ms bins



corrected spill

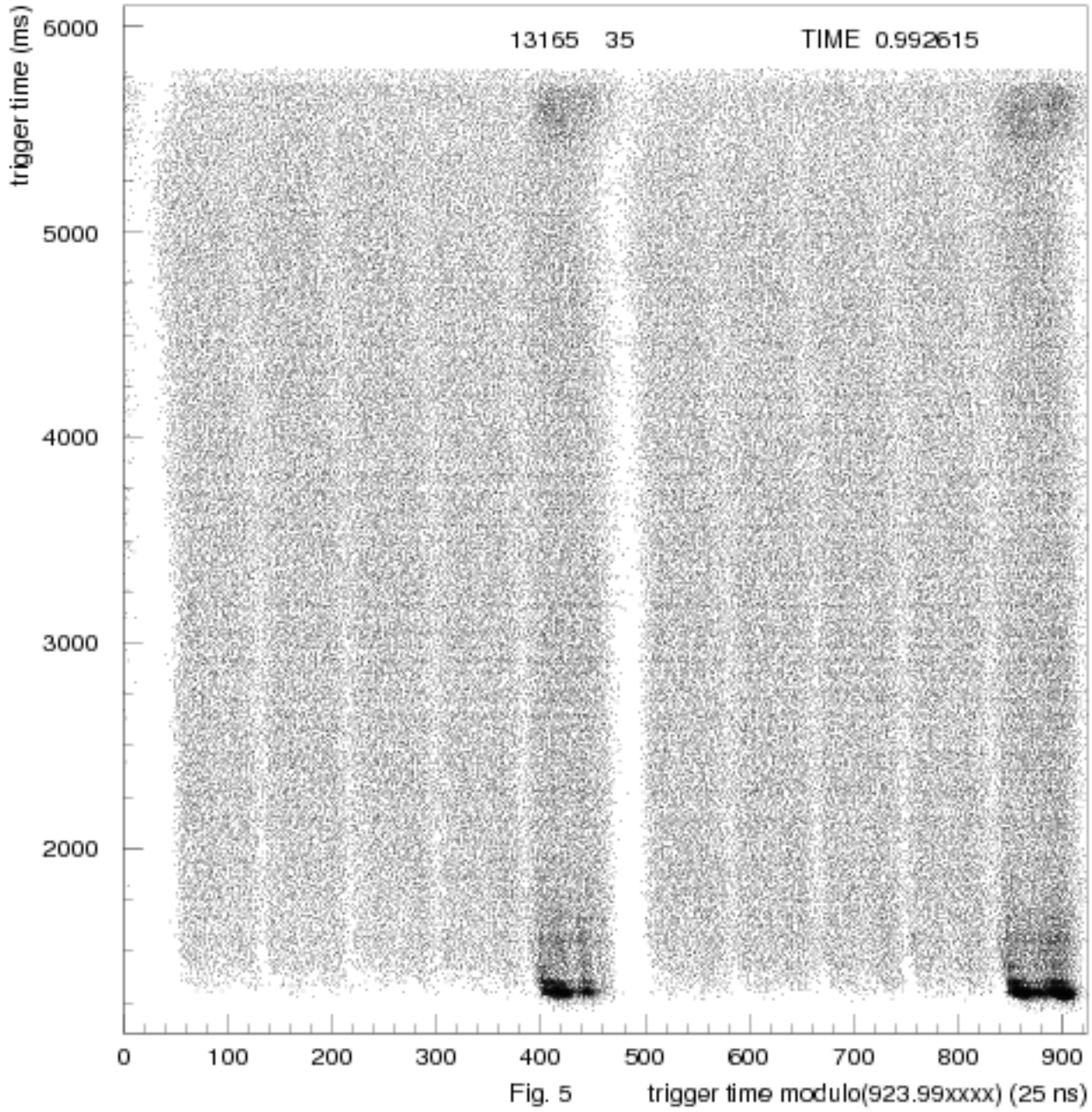
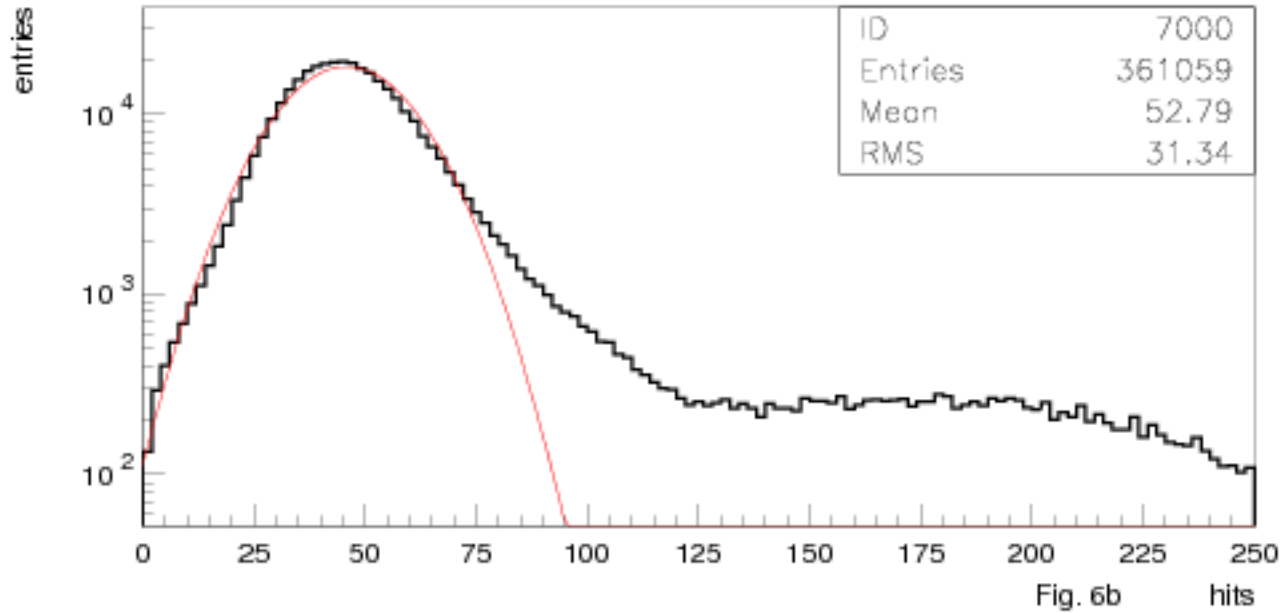
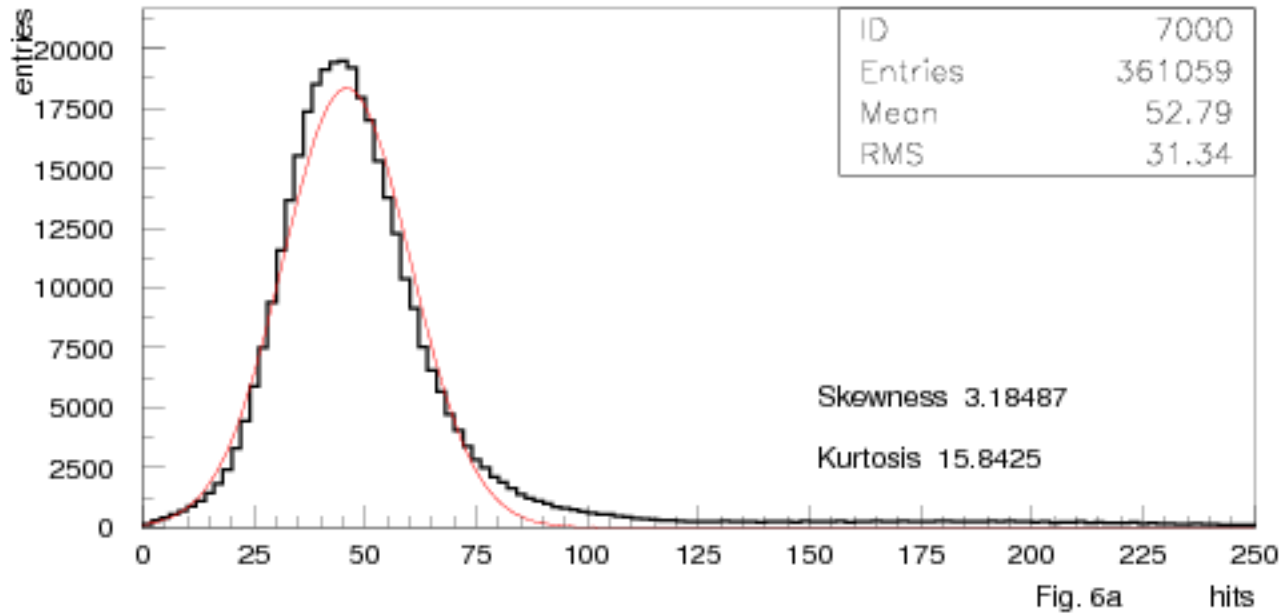


Fig. 5

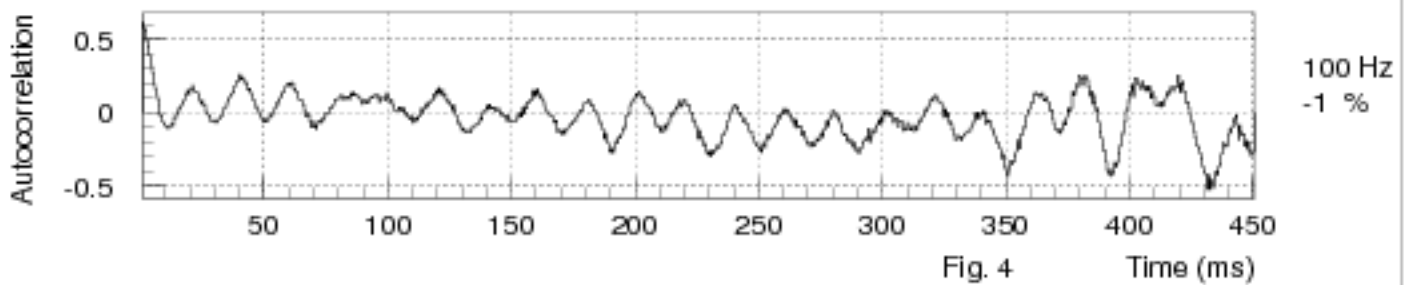
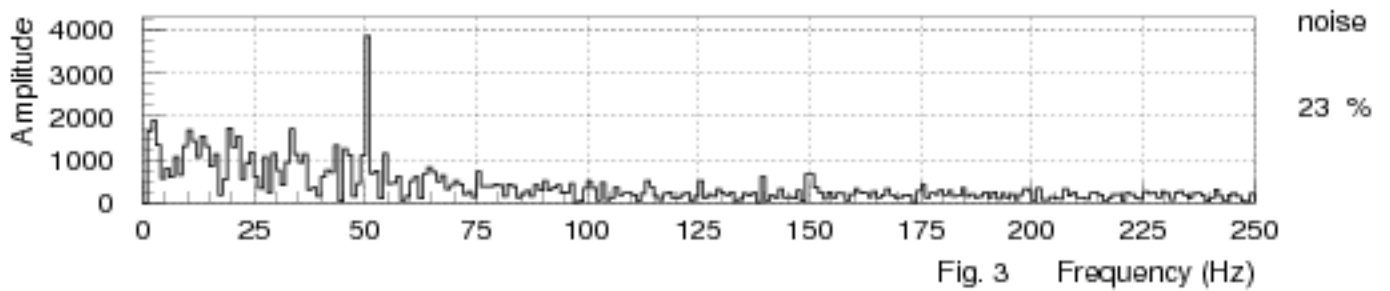
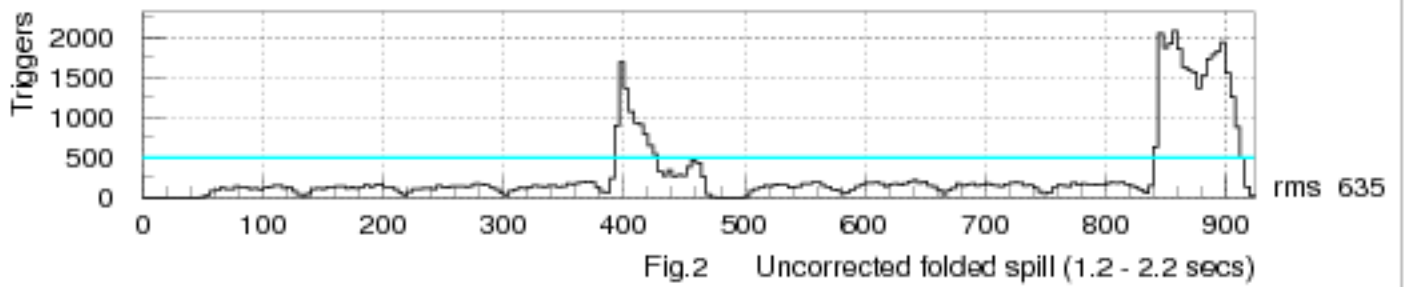
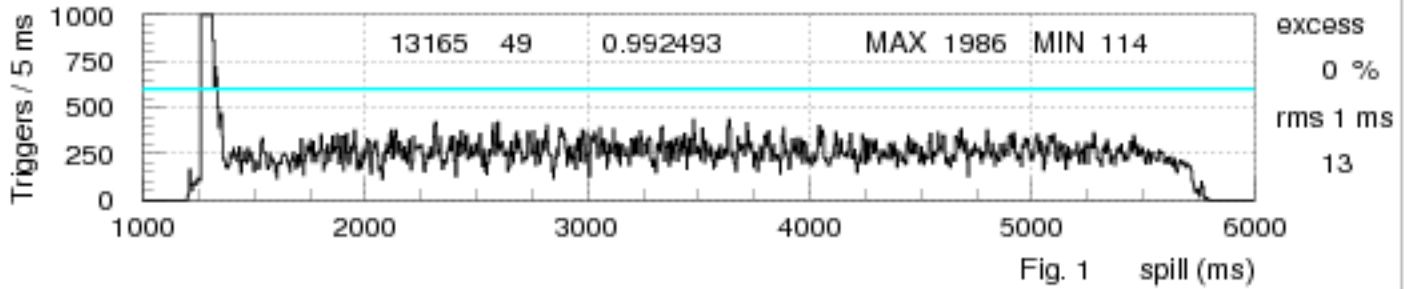
trigger time modulo(923.99xxxx) (25 ns)

## Hits GTK(1)

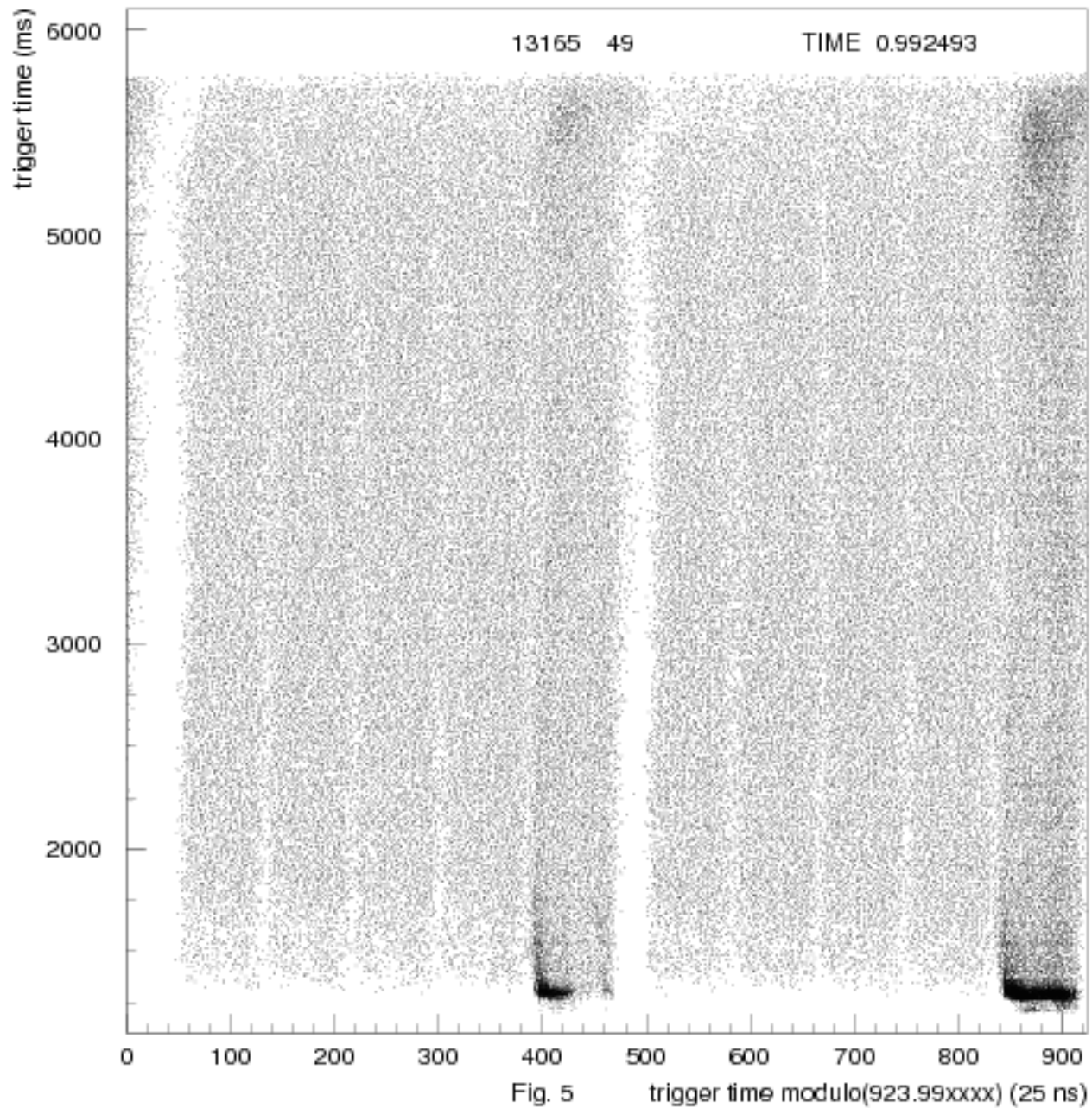




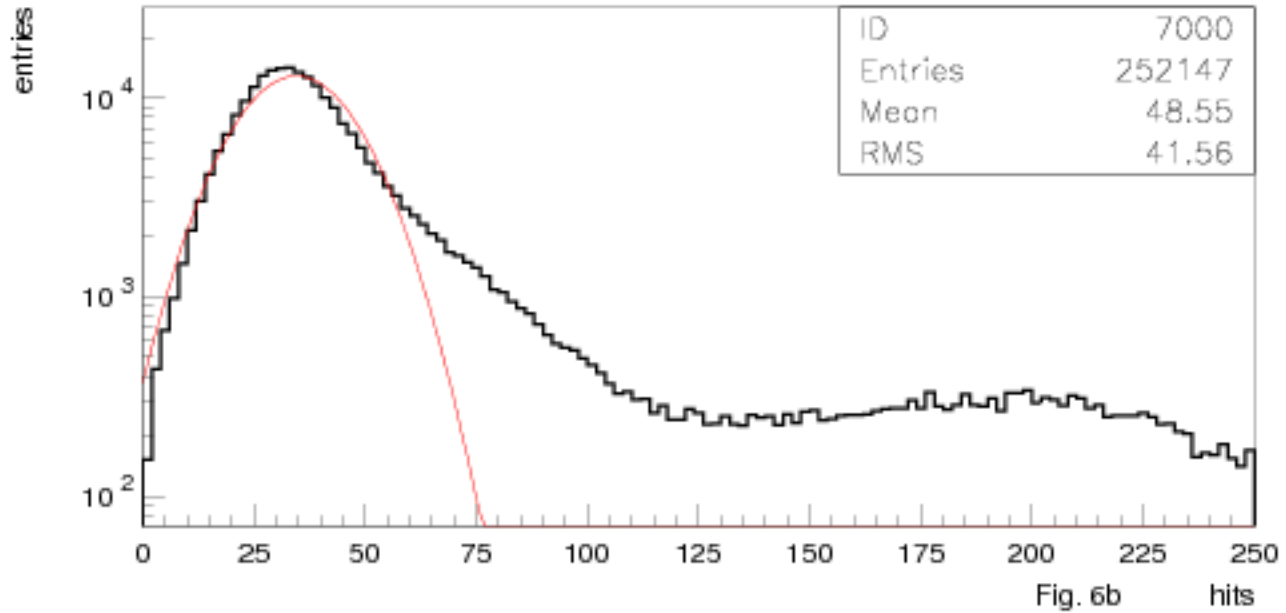
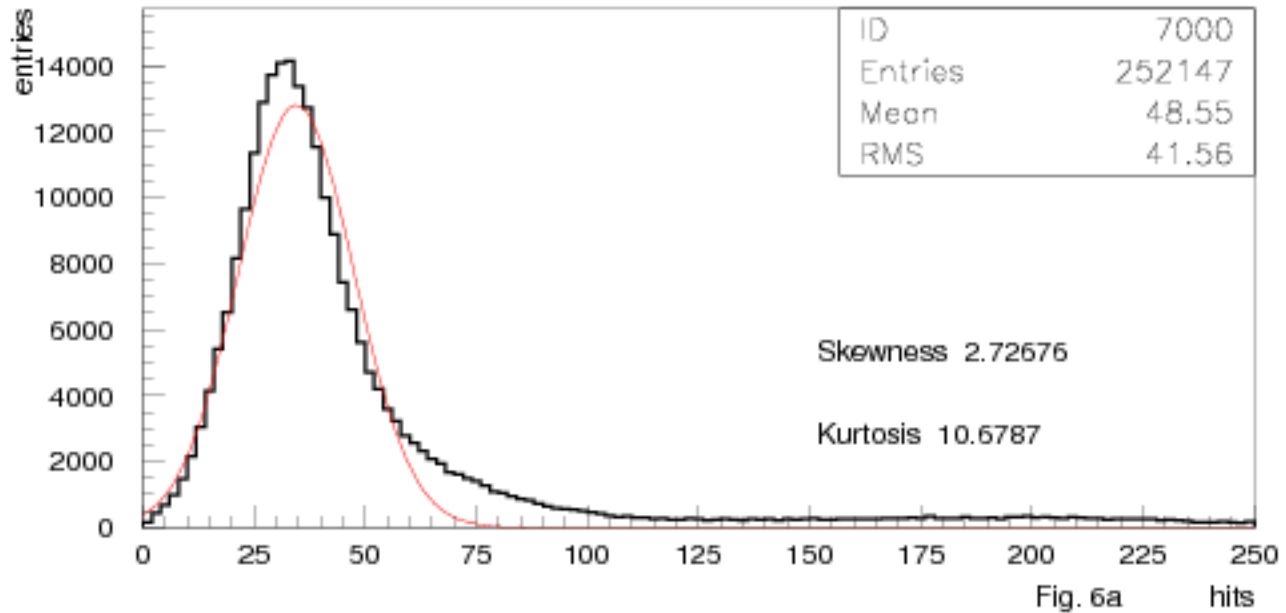
Spill - full range - 5 ms bins



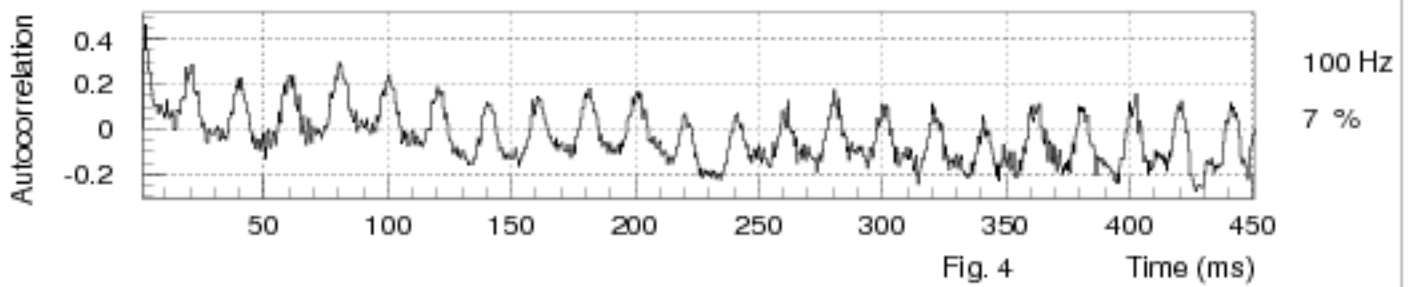
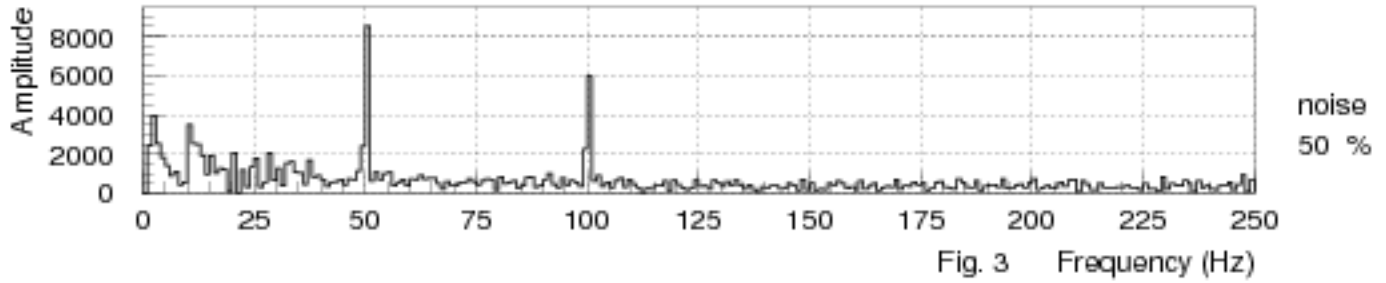
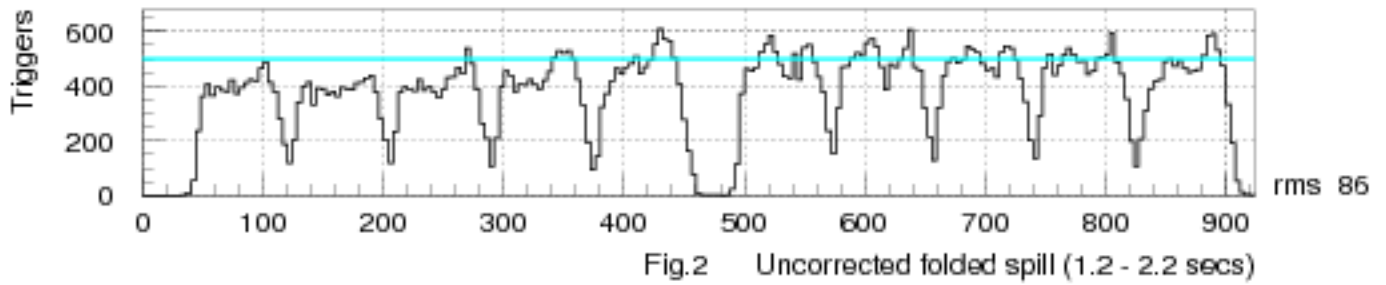
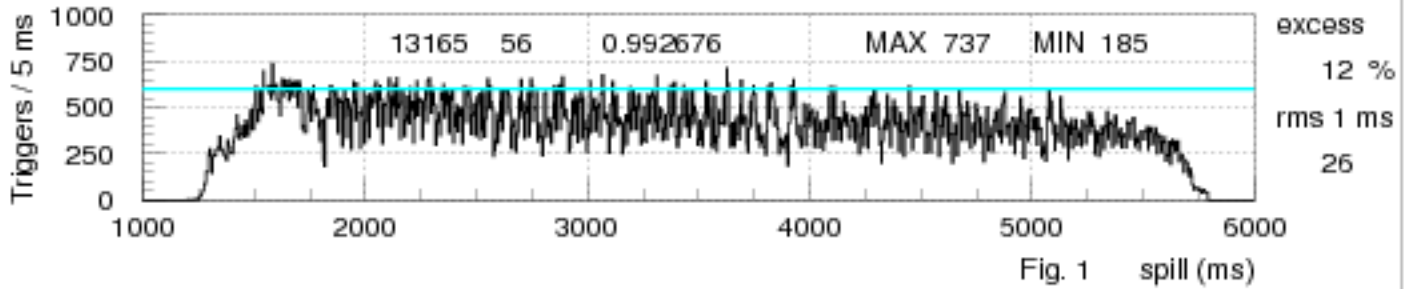
corrected spill



## Hits GTK(1)



Spill - full range - 5 ms bins



corrected spill

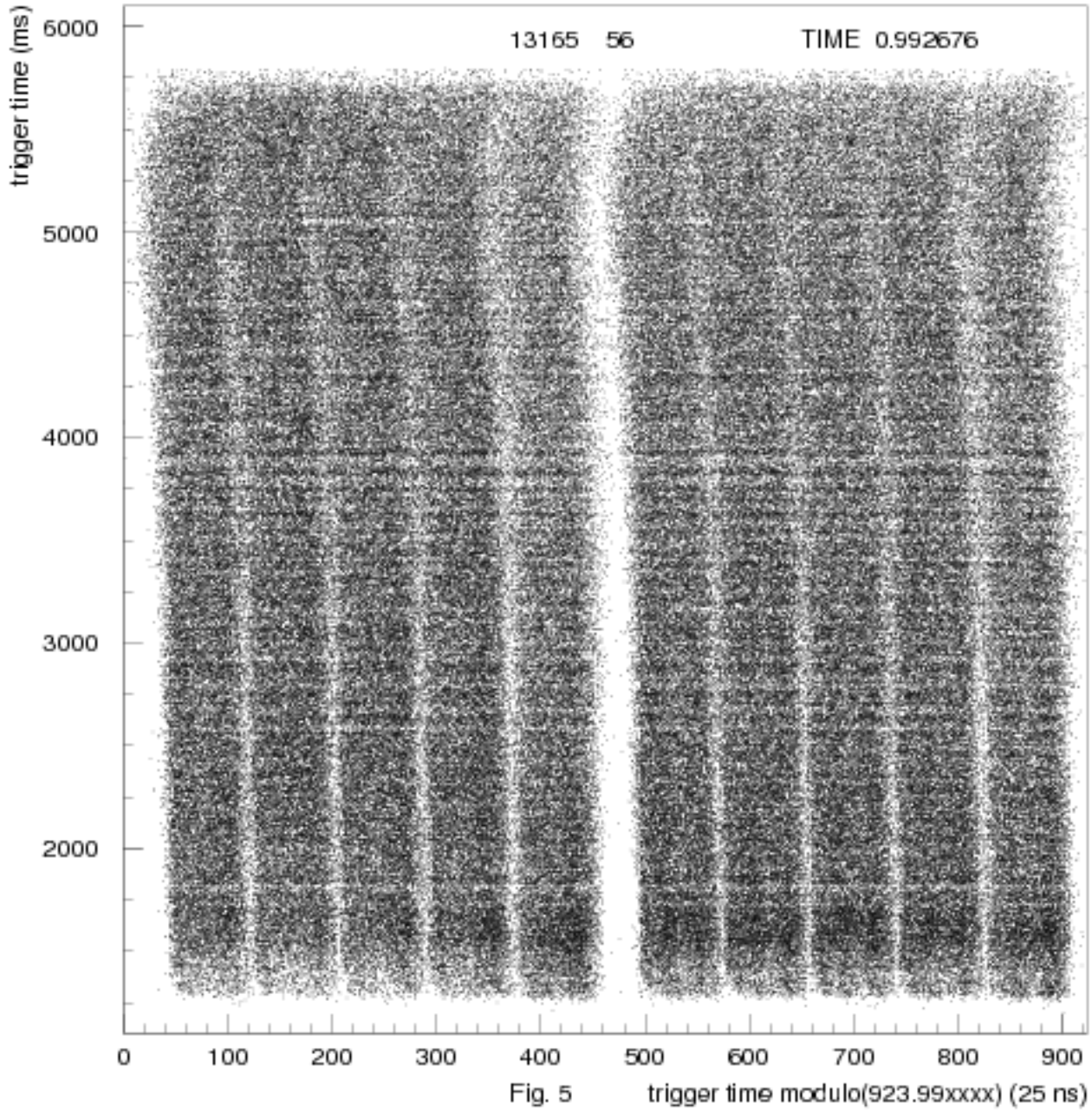
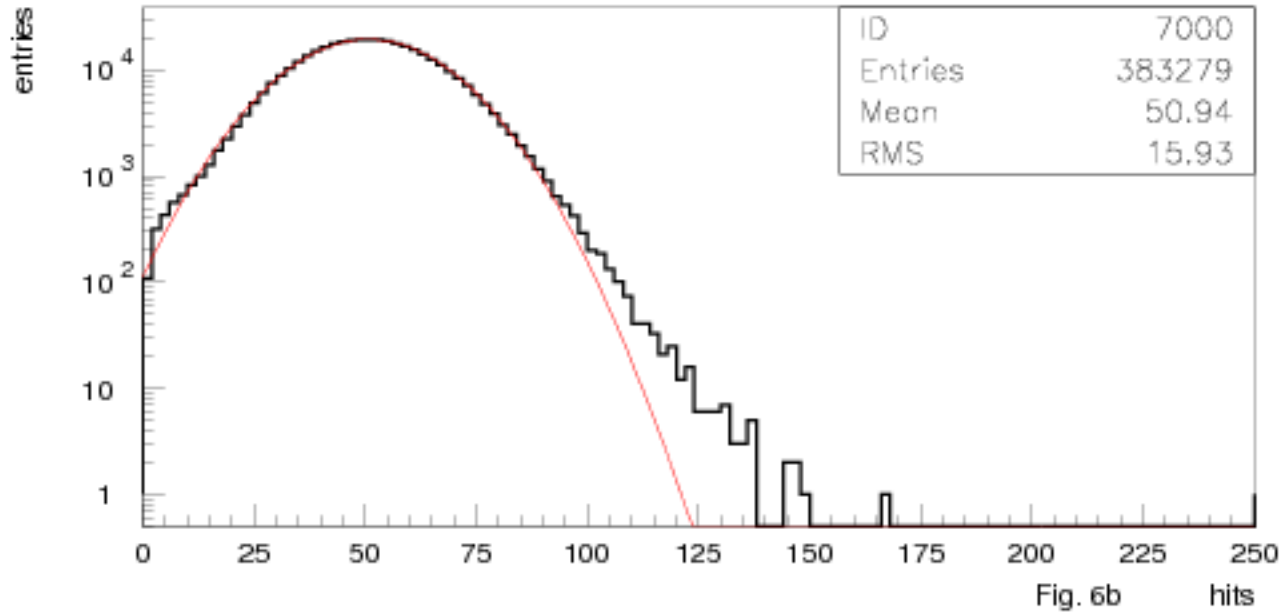
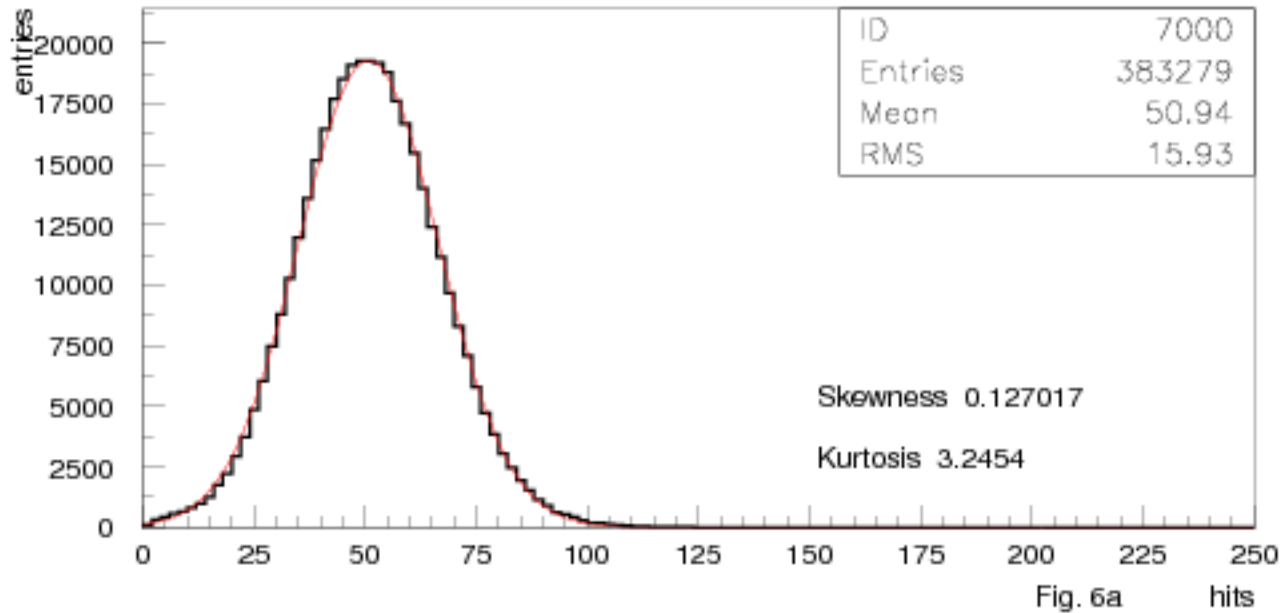


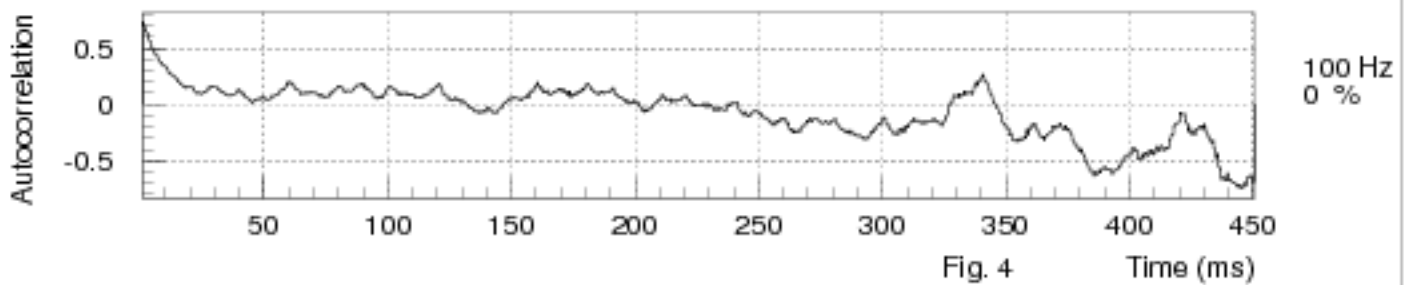
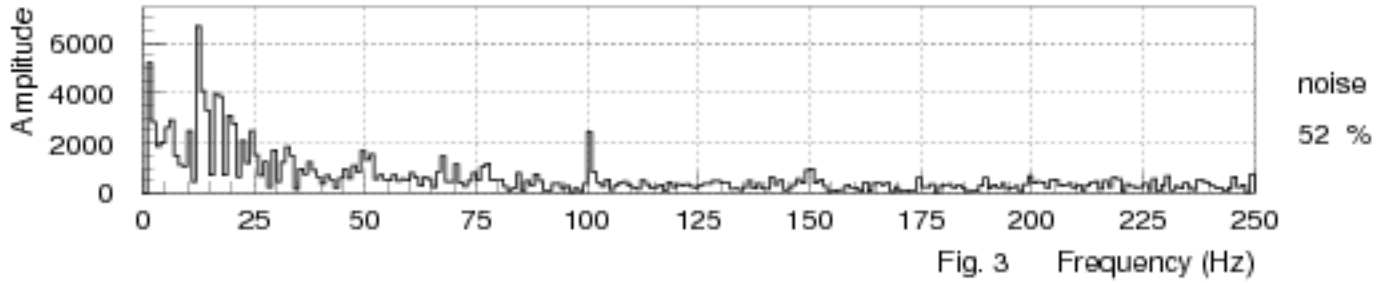
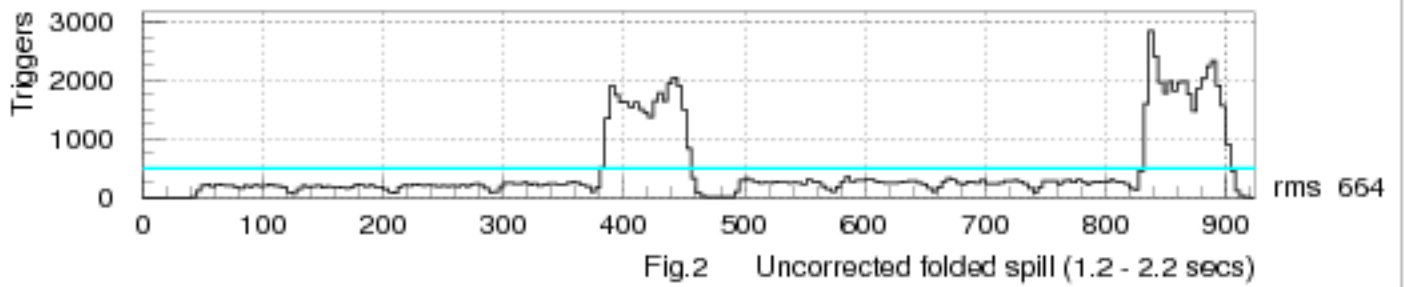
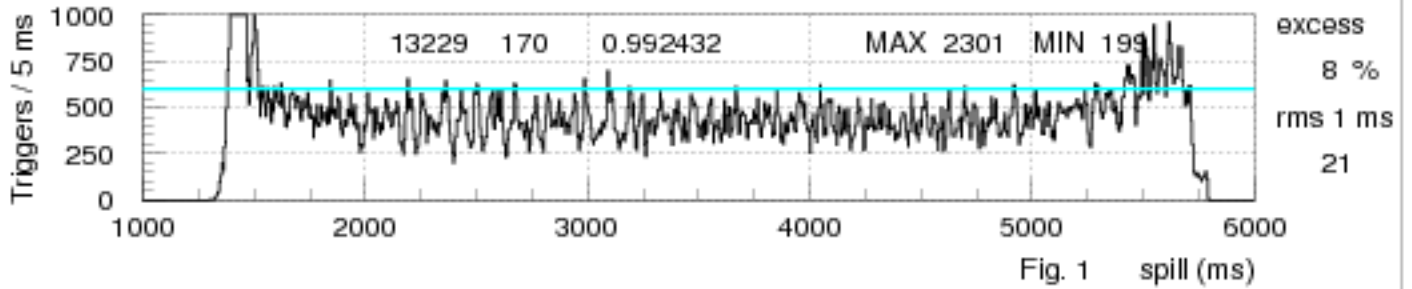
Fig. 5

trigger time modulo(923.99xxxx) (25 ns)

## Hits GTK(1)



Spill - full range - 5 ms bins





corrected spill

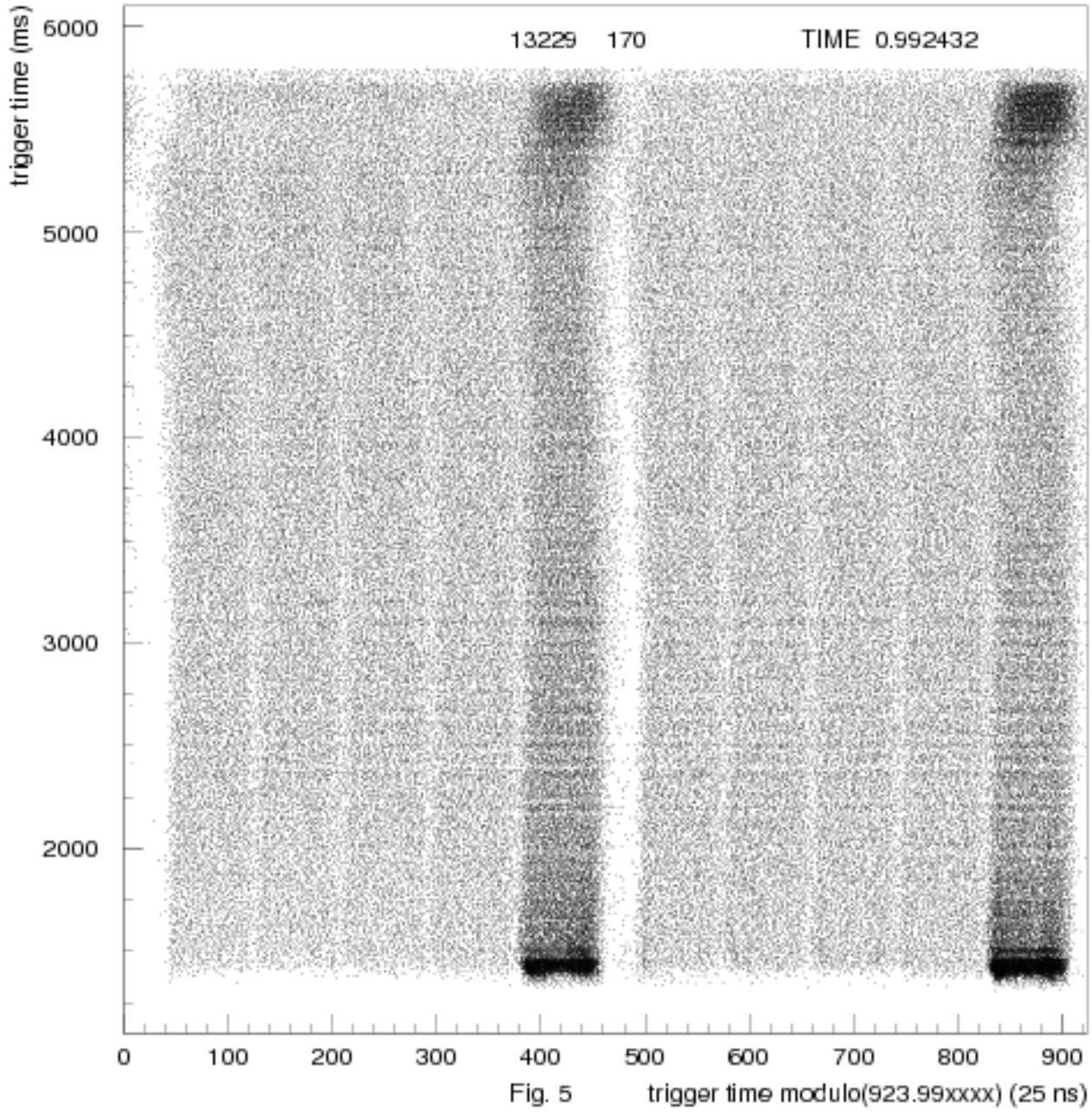


Fig. 5

trigger time modulo(923.99xxxx) (25 ns)

## Hits GTK(1)

